

Bio-Data of Dr. G.P. Biswas, Asstt. Prof., Dept. of Comp. Sc. & Engg.

Dr. Biswas received B. Sc Engineering Degree in Electrical and Electronic Engineering, M. Sc Engineering Degree in Computer Science & Engineering in 1983 and 1989, respectively, and the Ph. D Degree in Computer Science & Engineering from Indian Institute of technology, Kharagpur (IIT-Kgp) in 1998.

Since 1999, he has been with the Department of Computer Science & Engineering, Indian School of Mines (ISM), Dhanbad as an Assistant Professor. Before joining in ISM, he had been with the Department of Computer Science & Technology, Bengal Engineering College, Sibpur, West Bengal, a deemed university, where he was engaged in a project, sponsored by M/s Fujitsu microelectronic, USA, on the design of CA / PCA based BIST structure for VLSI (hard/soft) cores. Dr. Biswas teaches a number of B. Tech / M.Tech courses like Computer Organization, Theory of Computation, Computer Networks, VLSI Designs, etc. and has published around 18 research papers in Journal and conference proceedings. His current research interests include Cellular Automata, VLSI design and digital communication networks.