

Curriculum Vitae of Prasanta K. Jana

IEEE Senior Member

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Personal

Name: Prasanta K. Jana

Present Position: Professor & Head

Address: Department of Computer Science and Engineering

Indian School of Mines University, Dhanbad – 826 004, India

Date of Birth: 12 January, 1961

Nationality: Indian

Professional Qualifications

Ph. D. (Engineering) from Jadavpur University in 2000

M. Tech. (Computer Science) from Calcutta University in 1988

Specializations

Parallel and Distributed Computing

Current Research Areas

Parallel Algorithms, Optoelectronic Parallel Computing,
Interconnection Networks, Data Clustering, Computational Biology
& Wireless Sensor Networks.

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Recent Professional Activities

1. **Co-Chair** of National Seminar on Recent Advances on Information Technology (RAIT-2009), ISMU, Dhanbad, 6-7 February, 2009
2. **Convener** of 1st Workshop on Parallel & Distributed Computing (WPDC-08), ISMU, Dhanbad, 28 March, 2008
3. **Convener** of National Seminar on Recent Advances on Information Technology (RAIT-2007), ISMU, Dhanbad, 26-27 February, 2007
4. **Reviewer** of
 - i) *J. of Parallel & Distributed Computing*
 - iii) *IEEE Trans. On Parallel & Distributed Systems*
 - iv) *IEEE Communication Letters*
 - v) *Algorithmica*
 - vi) *Pattern Recognition*
 - vii) *Intl. Journal of Computers & Applications (ACTA Press)*
 - viii) *Information Processing Letters*
5. **Associate Editor** of the Intl. Journal of Computer Engineering and Information Technology.
6. **Program Committee Member:**
 - i) *Intl. Conference ICCCT-2011*, MNNIT, Allahabad
 - ii) *Intl. Conference ICCCT-2010*, MNNIT, Allahabad
 - iii) *Intl. Conference EAIT-11*, Kolkata
 - iv) *Intl. Conference ICCTA-07*, Kolkata
 - v) *Intl. Conference EAIT-06*, Kolkata, 2006
 - vi) *41st National Convention*, CSI, 2006, Kolkata
 - vii) *Intl. Workshop IWDC-04*, ISI, Kolkata, 2004
7. **Ph. D Works Guided:**
 - i) Design of Parallel Algorithms and Architecture for Numeric and Non-numeric Problems by Dheeresh K. Mallick: Awarded.
 - ii) Mapping of Algorithms on Parallel Architectures by Kenny T. Lucas: Submitted
 - iii) Study and Design of Parallel Algorithms for Interconnection Networks by Sudhanshu Kr. Jha: Submitted.
 - iv) Developing Algorithms for Clustering Biological Data by Damodar Reddy: On going.
 - v) Design of Efficient Clustering Algorithms for Data Analysis by Sujit Kr. Chatterjee: On going.
 - vi) Design of Algorithms for Wireless Sensor Networks by Amgoth Tarachand: On going.

8. Books Reviewed:

- i) Computer Organization 6e by Hamacher et. al., McGraw Hill, 2010
- ii) Design and Analysis of Algorithms, Tata McGraw Hill, 2010
- iii) Advanced Computer Architecture by Hwang, McGraw Hill, 2008
- iv) Comp. Architecture and Parallel Processing by Hwang and Briggs, McGraw Hill, 2008

9. **Visiting Scientists** at ACM Unit, Indian Statistical Institute, Kolkata, 2003

10. Academic Foreign Visit:

- i) Las Vegas, USA in 2008
- ii) and University of Aizu, Wakamatsu, Japan in 2003

11. Invited Talks:

- i) “Parallel Algorithms on Optoelectronic Computers” at BIT, Mesra, Ranchi, 2008
- ii) At UGC-Academic Staff college, Ranchi University, 2006
- iii) “Parallel processing with Optoelectronic Computers”, at CDAC, Kolkata Centre, 2005
- iv) “Parallel & Distributed Computing” at DSMS College, Durgapur, West Bengal