

List of Publications of Dr G.P.Biswas:-

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- [2] G.P. Biswas, P. Dutta, P. Krishna and I. Sen Gupta, "Cellular Architecture for Affine Transforms on Raster Images", IEE Proc., Comput. Digit. Tech., 143(2):103-110, March 1996.
- [3] G.P. Biswas and I. Sen Gupta, "Design of t Error Correcting and all Unidirectional Error Detecting Codes using Cellular Automata", in Proc. First Fault-Tolerant Systems and Software, IIT Madras, India, pp. 134-140, November 1996.
- [4] G.P. Biswas and I. Sen Gupta, "Design of t-UED/AUED Codes from Berger AUED Codes", Proc. of the 10th International Conference on VLSI Design '1997, Hyderabad, India, pp. 364-369, January 1997.
- [5] G.P. Biswas and I. Sen Gupta, "A Design Technique of TSC Checker for Borden Code", Proc. of International Conference on VLSI Design '1997, Hyderabad, India, pp. 529-530, January 1997.
- [6] B.K. Sikdar, K. Paul, G.P. Biswas, V. Boppana, C. Yang, S. Mukherjee and P. Pal Chaudhuri, "Theory and Application of $GF(2^p)$ Cellular Automata as On-chip Test Pattern Generator", Proc. of International Conference on VLSI Design '2000, Kolkata, India, pp. 556-561, January 2000.
- [7] G.P. Biswas, "A New Method of Implementing CA-based Test Pattern Generator (CA-TPG)", No. 01-63, Cellular Automata 2001, pp. 182-186, Yokohama National University, Japan, November 2001.
- [8] G.P. Biswas, "An Efficient Technique of Signature Computation and the Minimization of its Aliasing", Proc. on National Seminar on Advances in Mathematical, Statistical and Computational methods in science and Technology, AM, ISM, India, November, 2001.
- [9] G.P. Biswas and R.K. Prasad, "An Algorithm for Word Recognition in ASCII using Cellular Automata", Proc. on National Seminar on Advances in Mathematical, Statistical and Computational methods in science and Technology, AM, ISM, India, November, 2001.
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- Exhibition on Role of IT-in the Present Scenario of Globalization (RIT-2002), February, 2002.
- [11] G.P. Biswas and Jilani, K.G., ‘The DotCom (.Com) Business: A Challenge To Computer IT’, All India Seminar on Challenges ahead with Information Technology, CAIT-2002, pp 01-05, SLET, Longowal, February, 2002.
 - [12] G.P. Biswas, “Modification of 3-Neighborhood Cellular Automata to 4-Neighborhood Cellular Automata to Increase their Capabilities”, Journal of CSI, Vol. 33, No. 3, PP 27-34, July-September, 2003.
 - [13] Biswas, G.P. and Sharma, S., ‘Efficient Caching of Web Pages Based on the Clustering of Users’ IP Addresses,’ *Proc. of the Workshop on Internet and Applications*, ISM, PP. 114-125, December 2003.
 - [14] Biswas, G.P., Nath, A., and Sharma, S., “Enhancement of AMES-BM Protocol for Accessing High-Speed Shared Channel,” *Proc. of the Workshop on Internet and Applications*, ISM, PP. 126-136, December 2003.
 - [15] Biswas, G.P. and Sharma, S., ‘Computer Aided Design (CAD) for Fabricating MOS Devices’, *National Conference on Emerging Areas in Applied Physics (NCEAAP) (published in Asian Journal of Physics)*, 21-23 February, ISM, Dhanbad, 2004.
 - [16] G.P. Biswas and T Guha, “Collision Resolution in Multiaccess-Channel using Dynamic Multiplicities”, *Proc. of IEEE INDICON-2004*, PP-127-130, December 2004.
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 - [18] G.P. Biswas, Vishnu Sharma and R.K Mishra, “On Implementation of Large/Max-Length Cellular Automata Using 4-Neighborhood Cellular Automata (4-N CA)”, *International Conference on Emerging Applications of IT (EAIT-2006)*,